
Bibliographie

1. **Stéphane Azou**, " Réalisation équilibrée de systèmes par orthogonalisation de fonctions d'entrée - Grammiens et Approximation ", Doctorat en Electronique, l'université de Bretagne Occidentale, NO: 545, Année 1997
2. **Rabhi Lakder**, " Réduction des systèmes de grande dimension par la méthode de Krylov" thèse d'ingénieur, Centre Universitaire de Djelfa, Septembre 2003.
3. **André Schneider**, "Matrix decomposition based approaches for model order reduction of linear systems with a large number of terminals", Diploma Thesis, Department of Mathematics, Chemnitz University of Technology, April 2008.
4. **Amel. B. H . Adamou-Mitiche**, " Contribution à la théorie d'approximation des systèmes descripteurs", thèse de Doctorat d'Etat, Ecole Nationale Polytechnique d'Alger, 2004.
5. **L. T. Pillage and R. A. Rohrer**, " *Asymptotic waveform evaluation for timing analysis*", IEEE Trans. On Computer-Aided Design of Integrated Circuits and Systems, April 1990.
6. **P. Feldmann and R. W. Freund**, " *Efficient linear circuit analysis by Padé approximation via the Lanczos process*", IEEE Trans. on Computer-Aided Design of Integrated Circuits and Systems, vol. 14, no. 5 , May 1995.
7. **M. Silveira, M. Kamon, I. Elfadel, and J. White**, "A coordinate-transformed Arnoldi algorithm for generating guaranteed stable reduced-order models of RLC circuits", in Proc. Int. Conf. on Computer Aided Design (ICCAD), 1996.
8. **A. Odabasioglu, M. Celik, Lawrence T. Pileggi**, " *PRIMA: Passive reduced-order interconnect macro-modeling algorithm*", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol.17. No.8. August 1998.
9. **R. W. Freund**, " *SPRIM: structure-preserving reduced-order interconnect macro-modeling* ", in Proc. Int. Conf. on Computer Aided Design (ICCAD), 2004.
10. **P. Feldmann**, " *Model order reduction techniques for linear systems with large numbers of terminals* " in Proc. European Design and Test Conf. 2004.
11. **P. Feldmann and F. Liu**, " *Sparse and efficient reduced order modeling of linear subcircuits with large number of terminals* ", in Proc. Int. Conf. on Computer Aided Design (ICCAD), 2004.

12. **P. Liu, S. X.-D. Tan, H. Li, Z. Qi, J. Kong, B. McGaughy, and L. He**, "*An efficient method for terminal reduction of interconnect circuits considering delay variations*", in Proc. Int. Conf. on Computer Aided Design (ICCAD), 2005.
13. **Pu. Liu, Sheldon X.-D. Tan, Boyuan Yan, Bruce McGaughy**, "*An extended SVD_ based terminal and model order reduction algorithm* ", Cadence Design Systems Inc. CA 95134. SPSU. from IEEE Xplore, 2006.
14. **Patrick Siarry**, " *Automatique de base* ", édition ELLIPSE, octobre 1996.
15. **Zhaojun Bai**, " *Krylov subspace techniques for reduced –order modeling of large-scale dynamical systems* ", Applied Numerical Mathematics, Published by Elsevier Science, 2002,
16. **Roland. W. Freund**, "*Model reduction methods based on krylov subspaces* ", Acta Numerica, 2003.
17. **T. Stykel**, " *Numerical solution and Perturbation Theory for Generalized Lyapunov Equations*", Linear Algebra Appl. 2002.
18. **T. Stykel**, "Analysis and numerical solution of generalized Lyapunov equations ", Ph.D. Thesis, Mathematik und Naturwissenschaften, Universitat Berlin, 2002.
19. **Roland. W. Freund**, " *Krylov subspace methods reduced order modeling in circuit simulation* ", Journal of Computational and Applied Mathematics 123, 2000.
20. **D.Cobb**, " *Controllability, Observability, and Duality in Singular systems* ", IEEE Trans. On Autom. Cont, vol. 29, No. 12, December 1984.
21. **T. Sykel**, " *On criteria for asymptotic stability of differential-algebraic equations* ", z. Angewandte Mathematic und Mechanik, Vol. 82/03, 2002
22. **V.L. Syrmos, P. Misra and R. aripirala**, " *On the discrete Generalized Lyapunov equation*", Automatica, Vol. 2 , 1995.
23. **Dai, L**, "*Singular Control Systems* ", Lecture Notes in Control and Information Sciences, 118. Springer, Berlin Heidelberg New York, 1989.
24. **T. Stykel**, " *Gramian-based model reduction for descriptor systems* ", Mathematics of Control Signals Systems, 2004
25. **Girish Parmar, Manisha Bhandari**, "*Reduced Order Modelling of Linear Dynamic Systems using Eigen Spectrum Analysis and Modified Cauey Continued Fraction* ", XXXII National Systems Conference, NSC, December 17-19, 2008
26. **Pu Liu, Sheldon X.-D. Tan, Bruce McGaughy, Lifeng Wu, and Lei He**, " *Term-Merg: An Efficient Terminal-Reduction Method for Interconnect Circuits* ", IEEE

- transactions on computer-aided design of integrated circuits and systems, vol. 26, no. 8, August 2007
27. **Zoran Ilievski, Eindhoven**, "*Model Order Reduction and Sensitivity Analysis*", European Commission in the framework of the CoMSON RTN project, grant number MRTN-2005-019417.
 28. **Matlab**, Copyright 1984-2010, the math-works. Inc Version 7.10.0 (R2010a), 02/2010.
 29. **Younes Chahlaoui, Paul Van Dooren**, "*A collection of Benchmark exemples for model reduction of linear time invariant dynamical systems*", SLICOT Working Note. February 2002.
 30. **Erwan Liberge**, "*Réduction de modèles par POD-Galerkin pour les problèmes d'interaction fluide-structure*", thèse Doctorat en mécanique, l'Université de La Rochelle, Fevrier 2008.
 31. **J.-M. Arnaudiès, H. Fraysse**. "*Cours de mathématiques*". Dunod, Paris 1989.
 32. **Jody S. Hourigan and Lynn V. McIndoo**, "*The Singular Value Decomposition*", Linear Algebra- Math 45.